

ABSTRACT

1 A power supply circuit comprising an active power factor
2 correction circuit. The active power factor correction
3 circuit comprises a controller for controlling both the
4 power factor correction and the inrush current control
5 circuit. The inrush control circuit comprises at least
6 one insulated gate bipolar transistor (IGBT) connectable
7 to the controller.

09/04/2004 14:23:00